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## PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of ) Atty. Docket No. ARTCP031  
TEMPLETON et al. )  
Application No. 10/074,517 ) Examiner: Unassigned  
Filed: February 12, 2002 )  
For: SYSTEM AND METHOD FOR ASSURED ) Group Art Unit: 2812  
BUILT IN SELF REPAIR OF MEMORIES )  
Date: April 3, 2003

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on April 3, 2003

Signed:

Neely (Jo Weld)

INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR §§1.56 AND 1.97(c)

Commissioner for Patents  
Washington, DC 20231

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make the attached references of official record in this application.

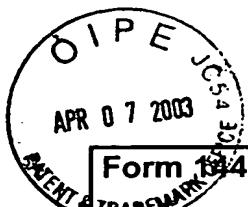
This Information Disclosure Statement (IDS) is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that this reference indeed constitutes prior art.

This Information Disclosure Statement is believed to be filed before the mailing date of a first Office Action on the merits. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-0805 (Order No. ARTCP031).

Respectfully submitted,  
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**Form 1649 (Modified)**
**Information Disclosure Statement By Applicant**

(Use Several Sheets if Necessary)

 Attorney Docket No: U.S.  
 ARTCP031 10/074,517  
 Applicant: TEMPLETON et al.  
 Filing Date: February 2, 2002  
 Group: 2812

**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	6,246,618	06/2001	Yamamoto et al.			
	B	5,936,977	08/1999	Churchill et al.			
	C						
	D						
	E						
	F						
	G						
	H						
	I						
	J						
	K						

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	L							
	M							
	N							
	O							
	P							

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	R	KIM et al., "Built In Self Repair for Embedded High Density SRAM," Proceedings of the International Test Conference 1998. ITC '98. Washington, DC, October 19-20, 1998, International Test Conference, New York, NY: IEEE, U.S. Vol. Conf. 29, October 19, 1998, pages 1112-1119
	S	DREIBELBIS et al., "Processor-Based Built-In Self-Test for Embedded DRAM," IEEE Journal of Solid-State Circuits, IEEE Inc, New York, US, Vol. 33, No. 11, November 1998, pages 1731-1740
	T	
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.